

**Notice of References Cited**

Application/Control No.

09/625,578

Applicant(s)/Patent Under  
Reexamination  
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Examiner

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2123

Page 1 of 1

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
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	W	Smith, "Packaging and power distribution design considerations for a Sun Microsystems desktop workstation", IEEE, October 1997.
	X	Bertacco et al., "Cycle based symbolic simulation of gate level synchronous circuits", IEEE 1999.

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.